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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC715 U.S. PTO
09/716493
11/20/00

In re the Application of:

TI-30026

Gilbert Laurenti, et al.

Serial No:

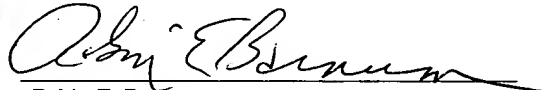
Filed: November 20, 2000

For: Processor With Pointer Tracking to Eliminate Redundant Memory
Fetches

INFORMATION DISCLOSURE STATEMENT

Ass't Commissioner for Patents
Washington, DC 20231

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Robin E. Barnum

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. References have also been included from the Foreign Filing Search. Copies of the noted references are enclosed herewith.

Since this IDS is being submitted for any Office Action, no fee is due.

Respectfully submitted,



Gerald E. Laws
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